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# **PRODUCT CHANGE NOTICE**

**Data Sheet Specification  
Change for Intersil Product  
CA5420AMZ\***

**Refer to:  
PCN13052**

**Date: September 25, 2013**

September 25, 2013

To: Our Valued Intersil Customer

Subject: **Data Sheet Specification Change for Intersil Product CA5420AMZ\***

This notice is to inform you that Intersil has updated the data sheet specification for the listed CA5420AMZ\* products. The change to the *Electrical Specification* – *CMRR* limit aligns the data sheet with the product characteristics and is necessary to maintain product manufacturability in support of customer delivery requirements. Details regarding the change are contained on the following page. The updated data sheet is available on the Intersil web site at

<http://www.intersil.com/content/dam/Intersil/documents/fn19/fn1925.pdf>.

Products affected:

CA5420AMZ            CA5420AMZ96  
CA5420AMZR5428   CA5420AMZ96R5428

There have been no changes made to the die/silicon. There will be no change in external marking of the packaged parts. Parts affected by this change are identifiable via Intersil's internal traceability system.

Intersil will take all necessary actions to conform to agreed upon customer requirements and to ensure the continued high quality and reliability of Intersil products being supplied. Customers may expect to receive product electrically screened to the revised data sheet beginning *ninety* days from the date of this notification or earlier with approval.

If you have concerns with this notice, Intersil must hear from you promptly. Please contact the nearest Intersil Sales Office or call the Intersil Corporate line at 1-888-468-3774, in the United States, or 1-321-724-7143 outside of the United States.

Regards,



Jeffrey Touvell  
Intersil Corporation

PCN13052

CC: M. Carmody    P. Lee            T. Lok

# PCN13052 Data Sheet Updates

## From:

**Electrical Specifications** For Equipment Design at  $V_{SUPPLY} = \pm 1V$ ,  $T_A = +25^\circ C$ , Unless Otherwise Specified.

PARAMETER	SYMBOL	TEST CONDITIONS	CA5420A			UNITS
			MIN (Note 6)	TYP	MAX (Note 6)	
Input Offset Voltage	$V_{IO}$		-	2	5	mV
Input Offset Current	$ I_{IO} $		-	0.01	4	pA
Input Current	$ I_I $		-	0.02	5	pA
Large Signal Voltage Gain	$A_{OL}$	$R_L = 10k\Omega$	10	100	-	kV/V
			80	100	-	dB
Common Mode Rejection Ratio	CMRR		-	560	-	$\mu V/V$
			60	65	-	dB

# PCN13052 Data Sheet Updates

## To:

**Electrical Specifications** For Equipment Design at  $V_{SUPPLY} = \pm 1V$ ,  $T_A = +25^\circ C$ , Unless Otherwise Specified.

PARAMETER	SYMBOL	TEST CONDITIONS	CA5420A			UNITS
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Large Signal Voltage Gain	$A_{OL}$	$R_L = 10k\Omega$	10	100	-	kV/V
			80	100	-	dB
Common Mode Rejection Ratio	CMRR		-	560	-	$\mu V/V$
			50	65	-	dB