Notification Number:	20200601000A	Notification Date:	July 30, 2020
Title:	Datasheet for DAC38RF82, DAC38RF89		
Customer Contact:	Notification Manager	Dept:	Quality Services

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification.
The product datasheet(s) is being updated as summarized below.
The following change history provides further details.

TEXAS INSTRUMENTS

DAC38RF82, DAC38RF89

SLASEA6D – FEBRUARY 2017 – REVISED JUNE 2020

Changes from Revision C (August 2017) to Revision D **Page**

- Changed *Feature* From: Maximum lane rate: 12.5 Gbps To: DAC38RF89: Maximum lane rate: 12.5 Gbps 1
- Changed *Feature* From: Maximum lane rate: 12.5 Gbps To: DAC38RF82: Maximum lane rate: 12.8 Gbps 1
- Changed From: maximum bit rate of 12.5 Gbpss To: maximum bit rate of 12.5 Gbps (DAC38RF89) and 12.8 Gbps (DAC38RF82) the *Description* 1
- Added note to T_J in the *Absolute Maximum Ratings* 9
- Changed the f_{SerDes} MAX value From: 12.5 Gbps To: 12.8 Gbps for DAC38RF82 12
- Changed From: maximum bit rate of 12.5 Gbps. To: maximum bit rate of 12.5 Gbps (DAC38RF89) and 12.8 Gbps (DAC38RF82) in the *Overview* 25
- Changed from: SerDes lane is 12.5 Gbps To: SerDes lane is 12.5 Gbps (DAC38RF89) and 12.8 Gbps (DAC38RF82) in *SerDes Rate* 28
- Deleted *JESD204B Subclass 0 Support* 42
- Added *Writing to Reserved Bits* 68
- Changed 0x04 and 0x05 From: 0x0000 To: variable in Table 47 68
- Added Note 1 to Table 47 68
- Changed 0x7F From: 0x0008 To: 0x0009 in Table 47 68
- Changed 0x0D From: 0x8300 To: 0x8000 in Table 47 68
- Changed 0x0F From: 0x1F83 To: 0xFFFF in Table 47 68
- Changed 0x32 and 0x33 From: 0x0800 To: 0x0400 in Table 47 69
- Changed 0x23 From: 0x03F3 To: 0xFFFF in Table 47 70
- Changed 0x3B From: 0x0002 To: 0x1802 in Table 47 70
- Changed From: [reset = 0x0000] To: [reset = variable] in SERDES Loss of Signal Detection Alarms Register (address = 0x04) [reset = variable] 74
- Changed From: [reset = 0x0000] To: [reset = variable] in SERDES Loss of Signal Detection Alarms Register (address = 0x04) [reset = variable] 75

- Changed From: [reset = 0x0008] To: [reset = 0x0009] in Vendor ID and Chip Version Register (address = 0x7F) [reset = 0x0009] 78
- Changed From: [reset = 0x1300] To: [reset = 0x8000] in JESD FIFO Control Register (address = 0x0D)[reset = 0x8000] 81
- Changed From: [reset = 0x0000] To: [reset = 0x0400] in Gain for path AB Register (address = 0x32) [reset = 0x0400] .. 95
- Changed From: [reset = 0x0000] To: [reset = 0x0400] in Gain for path CD Register (address = 0x33) [reset = 0x0400] .. 95
- Changed all bits From R To R/W in Figure 94 96
- Changed all bits From R To R/W in Figure 95 97
- Changed the Description of Bit 3:1 in Table 91 97
- Changed Bit 1 From: MIN_LATENCY_ENA To: Reserved in Table 97 101
- Changed the title of Figure 110 To: JESD Crossbar Configuration 2 Register (JESD_CROSSBAR2) 106
- Changed the title of Figure 111 To: JESD Alarms for Lane 0 Register (JESD_ALM_L2) 107
- Changed the title of Figure 111 To: JESD Alarms for Lane 5 Register (JESD_ALM_L5) 112
- Changed From: [reset = 0xF000] To: [reset = 0xFC03] in Clock Configuration Register (address = 0x0A) [reset = 0xFC03] 116
- Changed From: [reset = 0x8000] To: [reset = 0x2002] in Divided Output Clock Configuration Register (address = 0x0C) [reset = 0x2002] 117
- Added Note 1 to Table 120 118
- Changed the description of Bit 1 From: TBD To: Enables SPI SYSREF for Internal SYSREF Generator in Table 120 .. 118
- Added Note 1 to Table 121 118
- Added Note 1 to Table 122 119
- Changed From: [reset = 0x0002] To: [reset = 0x1802] in Serdes Clock Configuration Register (address = 0x3B) [reset = 0x1802] 126
- Changed the reset value of Bit 14:11 From: 0x0 To: 0111 in Table 131 126
- Changed Bit 4:2 From: BUSWIDTH To: Reserved in Table 134 128

The datasheet number will be changing.

Device Family	Change From:	Change To:
DAC38RF82, DAC38RF89	SLASEA6C	SLASEA6D

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/DAC38RF82

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this notification:

None.

Product Affected:

DAC38RF82IAAV	DAC38RF82IAAVR	DAC38RF89IAAV	DAC38RF89IAAVR

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
WW Change Management Team	PCN_ww_admin_team@list.ti.com

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property

right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.