



## Final Product Change Notification

202006005F01

**Issue Date:** 29-Jul-2020  
**Effective Date:** 26-Oct-2020

Here's your personalized quality information concerning products Digi-Key purchased from NXP. For detailed information we invite you to view this notification online



**This notice is NXP Company Proprietary.**

# QUALITY

### Management Summary

#### Change Category

- |  |   |  |   |  |
|--|---|--|---|--|
| <input type="checkbox"/> Wafer Fab Process   | <input type="checkbox"/> Assembly Process   | <input type="checkbox"/> Product Marking           | <input type="checkbox"/> Test Location  | <input checked="" type="checkbox"/> Design                         |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification  | <input type="checkbox"/> Test Process   | <input checked="" type="checkbox"/> Errata                         |
| <input type="checkbox"/> Wafer Fab Location  | <input type="checkbox"/> Assembly Location  | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input checked="" type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware            | <input type="checkbox"/> Other              |  |   |  |

MC56F823xx Maskset Update to 3N33G for Design Fix

### Description of Change

NXP Semiconductors is announcing a new version of silicon for the MC56F823xx. The new silicon mask revision is 3N33G.

3N33G mask set fixed errata e11484 POR: Residual voltage on VDD may cause POR unsuccessful.

Errata document was updated to Rev. 02 Aug 2018.  
Changes are summarized below:

added e9432: eFlexPWM: Fractional delay block may powerup with an output of 1 instead of 0.  
added e11484: POR: Residual voltage on VDD may cause POR unsuccessful.

For MC56F827xx, the datasheet has been updated to Rev. 4, 11/2018.

For MC56F823xx, the datasheet has been updated to Rev. 4, 05/2020.

For MC56F82348M, the datasheet has been updated to Rev. 2, 07/2020.

The revision history included in the updated documents provides a detailed description of the changes. The updated Datasheet and Errata can be found at:

[https://www.nxp.com/products/processors-and-microcontrollers/additional-processors-and-mcus/digital-signal-controllers/mc56f823xx-and-mc56f827xx-digital-signal-controllers:MC56F827xx?fsp=1&tab=Documentation\\_Tab](https://www.nxp.com/products/processors-and-microcontrollers/additional-processors-and-mcus/digital-signal-controllers/mc56f823xx-and-mc56f827xx-digital-signal-controllers:MC56F827xx?fsp=1&tab=Documentation_Tab)

Please contact your NXP sales or marketing representative for additional information.

### **Reason for Change**

The release of MC56F82xxx new maskset 3N33G to fix errata e11484.

The datasheet and errata have been updated to correct errors and / or provide additional technical clarification on some device features.

### **Identification of Affected Products**

Top side marking

The mask set marking on the package will change from 2N33G to 3N33G.

## **Product Availability**

### **Sample Information**

Samples are available from 20-Jul-2020

The following sample part numbers are available:

PC56F82748VLH

PC56F82746MLF

PC56F82733MFM

For samples with other part numbers, NXP will deliver within 6 weeks after we receive the request.

### **Production**

Planned first shipment 20-Oct-2020

## **Anticipated Impact on Form, Fit, Function, Reliability or Quality**

No impact on form, fit, function, reliability or quality.

### **Data Sheet Revision**

A new datasheet will be issued

### **Disposition of Old Products**

Existing inventory will be shipped until depleted

## **Timing and Logistics**

In compliance with JEDEC J-STD-046, your acknowledgement of this change is expected by 28-Aug-2020.

## **Contact and Support**

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

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Customer Focus, Passion to Win.

NXP Quality Management Team.

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**Affected OPN**

MC56F82313VLC